

The VLSI Implementation Of A GaAs GIC Switched Capacitor Filter

By Harry G. Oldland

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Oldland, Harry G. URI: Presented is the initial step for the eventual implementation of a programmable GIC switched capacitor filter in a GaAs VLSI

Analog Integrated Circuits and Signal Processing, 30, 149 157, 2002 C 2002 Kluwer Academic Publishers. Manufactured in The Netherlands. VLSI Implementation of Fuzzy

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Analog VLSI Implementation of Neural Systems. Editors: Mead, Carver, Ismail, Mohammed (Eds.)

Area-time upper bounds for the iterative solution of linear systems and for the inversion of matrices in VLSI models are obtained and compared to the known upper

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Very-large-scale integration (VLSI) is the process of creating an integrated circuit (IC) by combining thousands of transistors into a single chip.

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International Journal of Science and Research (IJSR) ISSN (Online): 2319-7064 Index Copernicus Value (2013): 6.14 | Impact Factor (2013): 4.438

ABSTRACT VLSI Implementation of Digital Circuits by OPINDER SHARMA May 2011 Advisor: Dr. Harpreet Singh Major: Computer Engineering Degree: Master of Science Digital

This paper presents a VLSI architecture to implement the forward and inverse two dimensional Discrete Wavelet Transform (DWT), to compress medical images for st